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Address bus lines is operated as binary numeral so to address memory location, one needs the same address lines as one need bits for representing Where d is the number of address lines. As given above in 12.a one chip has bytes, to address them we need address lines. Therefore lines of address bus must be needed to access bytes of memory and address lines will be

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common to all chips.

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Since the word size in this architecture is 32, so data input part will have 32 bits. Therefore, number of bits in the data inputs of memory is 32. Number of bits in the address input . Since, the computer's memory unit has 256K words, which is, so the address inputs' contains 18 bits. Therefore, number of bits in the address inputs of ...

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Employed ...

MohammadHassan DehghanTazareh's Developer Story. I have started programming in 1996 when I was thirteen years old, on an Amiga 500, using AmigaBASIC and then Motorola 68000 Assembly language.

MohammadHassan DehghanTazareh - Stack Overflow

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