

## Vtu Design Lab Viva Questions And Answers

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### Department of Mechanical Engineering

VTU NOTES Tuesday, February 18, 2014. System software,Compiler Design and Unix lab viva questions INTRODUCTION. LEX. Lex is a program designed to generate scanners, also known as tokenizers, which recognize lexical patterns in text. It helps in writing programs whose control flow is directed by instances of regular expression in the input stream.

### System software,Compiler Design and Unix lab viva questions

other lab viva Here we provide all engineering department of All semesters i.e; 1st 2nd 3rd 4th 5th 6th 7th 8th Lab Viva Questions with answers. you can make use of it and prepare well for your lab viva exams. these are very important, these questions may be asked in your interview too & in your technical round also.

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### VTU Question Papers - VTU Resource

Viva Questions: CG & V Lab(10CSL67) 1. Define Computer graphics? Computer graphics are graphics created by computers and, more generally, the representation and manipulation of pictorial data by a computer. 2. Define Computer Animation? Computer animation is the art of creating moving images via the use of computers.

### Viva Questions: CG & V Lab(10CSL67) - gnanadeevice

Physics Lab(15PHYL17/27) Viva Questions: EXPERIMENTS: 1. Black box experiment; Identification of unknown passive electrical components and determine the value of Inductance and Capacitance 2. Series and parallel LCR Circuits (Determination of resonant frequency and quality factor) 3. I-V Characteristics of Zener Diode.

### Physics Lab (15PHYL 17/27) Viva Questions

Computer Networks VIVA Questions :-1. Explain What is Network? A network is a set of devices connected by physical media links. A network is recursively is a connection of two or more nodes by a physical link or two or more networks connected by one or more nodes. 2. What is a Link?

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Logic Design Laboratory Manual 3 \_\_\_\_\_ VIVA QUESTIONS: 1. Why NAND & NOR gates are called universal gates? 2. Realize the EX - OR gates using minimum number of NAND gates. 3. Give the truth table for EX-NOR and realize using NAND gates? 4. What are the logic low and High levels of TTL IC's and CMOS IC's? 5. Compare TTL logic family with CMOS family? 6.

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(Affiliated to VTU, Belgaum & Approved by AICTE, New Delhi) (NAAC Accredited & ISO 9001:2015 Certified Institution) NH 206 (B.H. Road), Gubbi, Tumkur - 572 216. Karnataka. System Software and Operating System Lab Manual-15CSL67 Department of Computer Science & Engineering VI Semester 2017-2018

### System Software and Operating System Lab Manual-15CSL67

Viva (5 marks per lab): There will be viva for each lab. The questions in the viva come from the lab material. No viva marks will be given if you are more than 10 minutes late. 3. Lab record (10 marks per lab): Students will write a lab report according to the format specified and turn it in at the beginning of the next lab session. Late lab ...

### B M S INSTITUTE OF TECHNOLOGY & MANAGEMENT

APPROVED BY AICTE NEW DELHI, AFFILIATED TO VTU BELGAUM ... Viva questions 47-48 7. Appendix-1 49-52 . 2 Syllabus DIGITAL ELECTRONICS LABORATORY [As per Choice Based Credit System (CBCS) scheme] ... Design and implement 4-bit Parallel Adder/ subtractor using IC 7483. 4. Design and Implementation of 4-bit Magnitude Comparator using IC 7485.

### DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING ...

Status of the Department: Grant in Aid VTU Recognized R & D Centre . Laboratories: Hydraulics Lab, Machine Shop, Production Technology Lab, Design Lab, Turbo Machine Lab, Photo- Elasticity Lab, Measurement Lab, Heat Transfer Lab, Metal Casting Lab, Metrology Lab, CAD/CAM Lab And IC Engines Lab. Other Facilities: Department Library .

### BMSECE | Mechanical Engineering

Viva questions 82 . VLSI Lab Manual VII sem, ECE 10ECL77 ... DIGITAL DESIGN 1. Write Verilog Code for the following circuits and their Test Bench for verification, observe the waveform and synthesize the code with the technological library, with the given Constraints\*. Do the initial timing verification with gate level simulation.

### VLSI lab manual VII sem, ECE - Gopalan Colleges

Viva Question Answers I / II SEMESTER SUB: ENGG. PHYSICS LAB SUB CODE: 15PHYL 17/27 PREPARED BY: Department of Engineering Physics . 1. Verification of Stefan's law by Electrical Method 1) State Stefan's law The amount of thermal radiation emitted per second per unit area of the surface of a black

### DEPARTMENT OF ENGINEERING PHYSICS

In this video I will give you most expected viva question asked in Practical exam .This is part I. ... Imp LAB-Viva Questions With Answer - Duration: 4:12. Electrical Engineer's Hub 8,966 views.

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